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Application Number	10604491
Filing Date	2003-07-25
First Named Inventor	Christos J. Georgiou, et al.
Art Unit	2111
Examiner Name	Kenneth S. Kim
Attorney Docket Number	

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/Kenneth Kim/

08/08/2007

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Attorney Docket Number	FIS920030113US1 (16497)	

KK	4	IBM Corporation: "CoreConnect bus architecture", http://www-3.ibm.com/chips/products/coreconnect/ ; last printed 9/20/06; pp. 1-2	<input type="checkbox"/>
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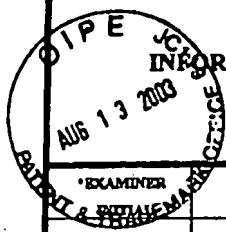
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FIS920030113US1 (16497)

Application Number

10/604,491

Applicant(s)

Christos J. Georgiou, et al.

Filing Date

07/25/03

Group Art Unit

Unassigned

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Ryu, et al., "A Comparison of Five Different Multi processor SoC Bus Architectures", Georgia Institute of Technology Electrical and Computer Engineering, Atlanta, GA Unknown date

Brinkman, et al., "On-Chip Interconnects for Next Generation System-on-Chips", Heinz Nixdorf Institute and Department of Electrical Engineering, University of Paderborn, Germany, Fraunhofer Institute of Microelectronic Circuits and Systems, Duisburg, Germany Unknown dated

Berry, "SYSTEM-ON-CHIP MARKETS AND TRENDS", Electronic Trend Publications, Inc. San Jose, CA, Second Edition, 2003, pp. 1-1 -5-2

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